

Claims

1. A method of controlling output from a power amplifier which has a driver stage and an output stage, the method comprising:
- 5 detecting a first and a second electrical parameter at an output of the output stage;
- processing the first parameter with a first reference signal to generate a bias control signal;
- 10 processing the second parameter with a second reference signal to generate a bias reduction signal;
- feeding the bias control signal to an input of both the driver and the output stages;
- 15 feeding the bias reduction signal to an input of the driver stage.
2. The method of controlling output from a power amplifier as claimed in claim 1, wherein the first
- 20 electrical parameter is an electrical current.
3. The method of controlling output from a power amplifier as claimed in claim 1, wherein the second
- 25 electrical parameter is an output voltage envelope.
4. An electrical circuit comprising:
- a power amplifier, an external control loop and a protection circuit; wherein the protection circuit comprises:
- 30 detection means for detecting an electrical parameter at an output of the power amplifier;
- bias reduction means for providing a bias signal at an input of the power amplifier;
- a reference signal generator coupled to the bias
- 35 reduction means.

5. The electrical circuit of claim 4, wherein the power amplifier has a driver stage and an output stage.

6. The electrical circuit of claim 5, wherein the
5 detection means is coupled to the output of the output stage.

7. The electrical circuit of claim 6, wherein the bias reduction means is coupled to the input of the
10 driver stage.

8. A mobile telecommunications device including the power amplification circuit as claimed in claims 4 to
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